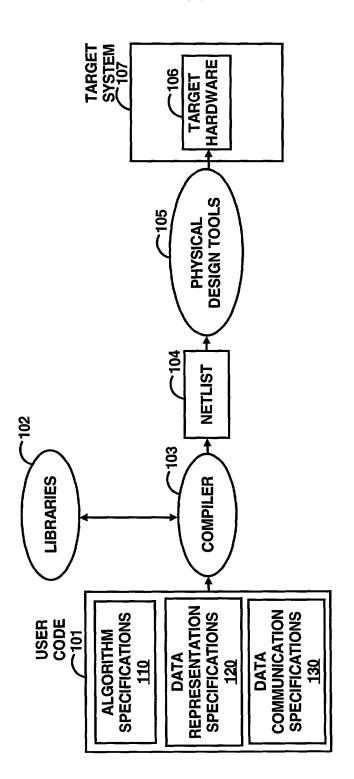
### AN ALGORITHM-TO-HARDWARE SYSTEM

AND METHOD FOR CREATING A DIGITAL CIRCUIT Richard J. CARTER

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100

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**200** 

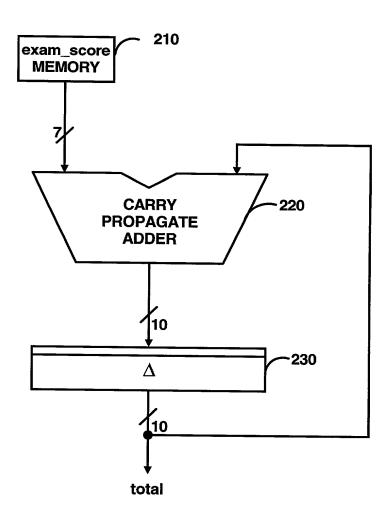


FIG. 2

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<u>300</u>

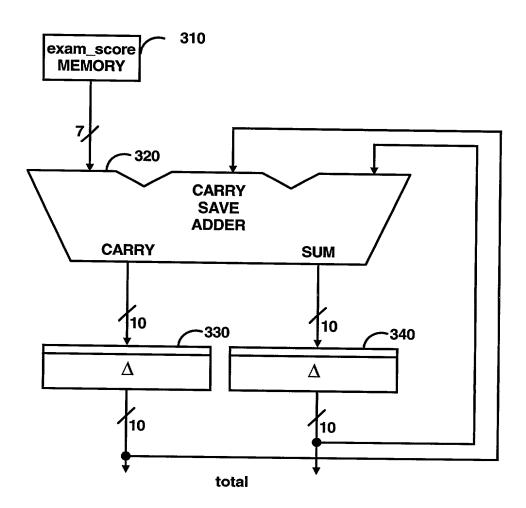


FIG. 3

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<u>400</u> 410 exam\_score **MEMORY** PARALLEL-420 **TO-SERIAL CONVERTER** 430 **CARRY SAVE ADDER CARRY SUM** 440 450 Δ 10∆

total

FIG. 4

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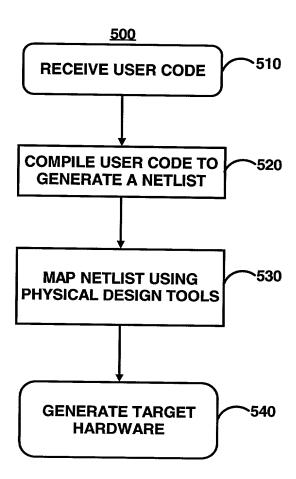


FIG. 5

## AN ALGORITHM-TO-HARDWARE SYSTEM AND METHOD FOR CREATING A DIGITAL CIRCUIT Richard J. CARTER HP Docket No.: 10008130-1

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<u>600</u>

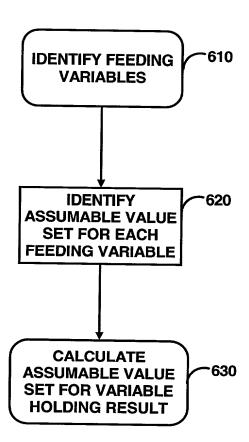


FIG. 6

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<u>700</u>

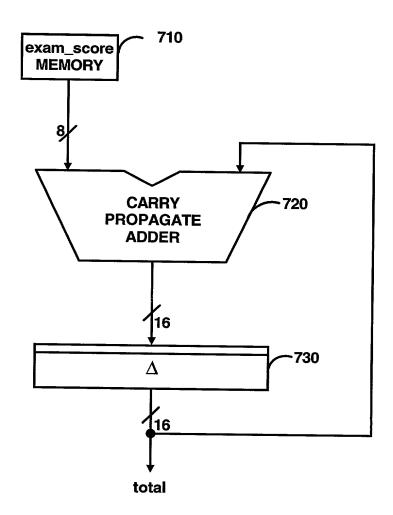


FIG. 7 Prior Art

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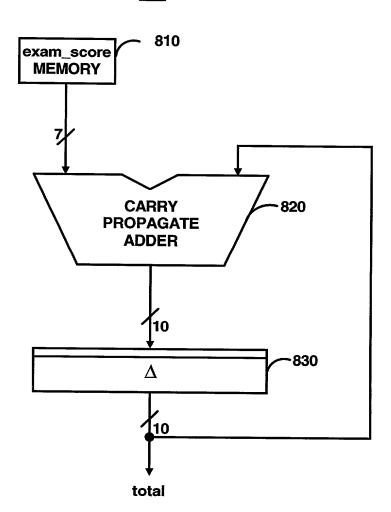


FIG. 8 Prior Art